

IN THE CLAIMS:

Kindly cancel claims 2-3 without prejudice or disclaimer. Kindly amend claims 1 and 4 as follows. A detailed listing of all claims is as follows.

Claim 1 (Currently Amended): A liquid crystal display including liquid crystal pixel cells arranged at each intersection between a plurality of gate lines and a plurality of data lines, comprising:

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- a thin film transistor associated with each pixel cell;
- a storage capacitor; and
- a smectic liquid crystal between an upper substrate and a lower substrate, wherein the smectic liquid crystal has spontaneous polarization in a range of 2nC/cm^2 to 100nC/cm^2 $2-70\text{nC/cm}^2$ and a unit storage capacitance is in a range of 1nF/cm^2 to 13nF/cm^2 $1-7\text{nF/cm}^2$ for optimizing transmittance depending on the spontaneous polarization of the smectic liquid crystal.

check for upper + in spec

Claims 2-3 (Cancelled)

Claim 4 (Currently Amended): A liquid crystal display including liquid crystal pixel cells arranged at each intersection between a plurality of gate lines and a plurality of data lines, comprising:

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- a thin film transistor associated with each pixel cell;
- a storage capacitor; and
- a smectic liquid crystal between an upper substrate and a lower substrate, wherein the smectic liquid crystal has ~~The liquid crystal display of Claim 1, wherein the spontaneous~~

$C_s = 0.21 - 1.47 \text{ nF}$
increase after Au layer tail

$70 - 100 \text{ nC/cm}^2$
 $5 - 13 \text{ nF/cm}^2$

103 in view of Example 4 of Yoshida

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end

polarization is in a range of $70\text{nC}/\text{cm}^2$ to $100\text{nC}/\text{cm}^2$ and the a unit storage capacitance is in a range of $5\text{nF}/\text{cm}^2$ to $13\text{nF}/\text{cm}^2$ for optimizing transmittance depending on the spontaneous polarization of the smectic liquid crystal.

70-100 nC/cm²
5-13 nF/cm²
